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	Application No.	Applicant(s)	•
	10/046,103	NUBER ET AL.	
Notice of Allowability	Examiner	Art Unit	•
	Boris Benenson	2836	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT F of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in this application or other appropriate communication RIGHTS. This application is subject	pplication. If not included on will be mailed in due cou	urse. <b>THIS</b>
1. This communication is responsive to 4/19/2004:	•		•
2. X The allowed claim(s) is/are 1 and 4-21.			*
3. $igotimes$ The drawings filed on <u>11/01/2001</u> are accepted by the Ex	aminer.		· .
4. Acknowledgment is made of a claim for foreign priority to a) All b) Some* c) None of the:  1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have 1. International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDONI THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  5. A SUBSTITUTE OATH OR DECLARATION must be submained including changes required by the Notice of Draftspering including changes required by the Notice of Draftspering including changes required by the attached Examined Paper No./Mail Date  [b) including changes required by the attached Examined Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in	ve been received. ve been received in Application No: _ cocuments have been received in this  of this communication to file a reply MENT of this application.  mitted. Note the attached EXAMINER ves reason(s) why the oath or declar ust be submitted. rson's Patent Drawing Review ( PTC  of 's Amendment / Comment or in the  1.84(c)) should be written on the draw	s national stage application of complying with the require R'S AMENDMENT or NOT ation is deficient.  O-948) attached Office action of the ba	rements
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT  Attached Examiner's comment regarding REQUIREMENT	osit of BIOLOGICAL MATERIAL	must be submitted. Note	e the
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Attachment(s)		, , , , , , , , , , , , , , , , , , ,	•
1. Notice of References Cited (PTO-892)	5. Notice of Informal I	Patent Application (PTO-1	52)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. Interview Summary Paper No./Mail Da		
3. Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date	7. Examiner's Amend	Iment/Comment	
4. Examiner's Comment Regarding Requirement for Deposit		ent of Reasons for Allowa	nce ·
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## Detailed Actions

1. Amendment received on 4/19/2004 is entered.

- 2. The Specification (Page 5, Line 27 Page 6, Line 3) is amended.
- 3. Claims 1, 4-10, and 15 are amended. Claims 2 and 3 previously cancelled. Claims 1 and 4-21 are pending in the Application.

## Allowable Subject Matter

4. Claims 1 and 4-21 are allowed.

The following is an examiner's statement of reasons for allowance:

5. Independent Claims 1 is allowable because none of the prior art of record disclose an integrated circuit comprising a buffer for reducing delays on relatively long conductive signal lines wherein the buffer includes a protection diode connected to a gate of one transistor of a first inverter of the buffer to prevent the buffer from being damaged by too much electrical charge on the gate in combination with the other claim limitations.

Claims 4-9 are dependent on Claim 1 and therefore allowable.

6. Independent Claim 10 is allowable because none of the prior art of record disclose a method for preventing a buffer used for

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reducing delays on relatively long conductive signal lines of an IC wherein the buffer includes a protection diode connected to a gate of one transistor of a first inverter of the buffer to prevent the buffer from being damaged by too much electrical charge on the gate and wherein the buffer and the protecting diode are formed on the IC in combination with the other claim limitations.

Claims 11-20 are dependent on Claim 10 and therefore allowable.

- 7. Independent Claim 21 is allowable because none of the prior art of record disclose a method for use in designing an integrated circuit comprising a step of inserting buffer cells for reducing delays on relatively long conductive signal lines wherein each buffer cell comprising first and second inverters and a protection diode connected to an input of the first inverter inside of the IC in combination with the other claim limitations.
- 8. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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## Contact information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Boris Benenson whose telephone number is (571) 272-2048. The examiner can normally be reached on M-F (8:20-6:00) First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571) 272-2800 x 36. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Boris Benenson Examiner Art Unit 2836

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